

DIN-68H-01

The DIN-68H-01 is a terminal board designed for PCIe/PXIe-5411 to provide the easier wiring for test circuit or measure signal.

Below is the layout and pin-to-pin reference table of DIN-68H-01:

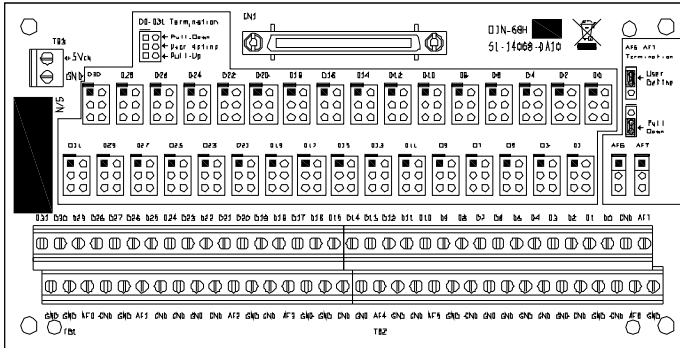


Figure A-1: DIN-68H-01 Layout

| | | | | | | | | |
|----------------|------|------|------|------|------|------|------|------|
| PCIe/PXIe-5411 | DIO0 | DIO1 | DIO2 | DIO3 | DIO4 | DIO5 | DIO6 | DIO7 |
| DIN-68H-01 | D0 | D1 | D2 | D3 | D4 | D5 | D6 | D7 |

| | | | | | | | | |
|----------------|------|------|-------|-------|-------|-------|-------|-------|
| PCIe/PXIe-5411 | DIO8 | DIO9 | DIO10 | DIO11 | DIO12 | DIO13 | DIO14 | DIO15 |
| DIN-68H-01 | D8 | D9 | D10 | D11 | D12 | D13 | D14 | D15 |

| | | | | | | | | |
|----------------|-------|-------|-------|-------|-------|-------|-------|-------|
| PCIe/PXIe-5411 | DIO16 | DIO17 | DIO18 | DIO19 | DIO20 | DIO21 | DIO22 | DIO23 |
| DIN-68H-01 | D16 | D17 | D18 | D19 | D20 | D21 | D22 | D23 |

| | | | | | | | | |
|----------------|-------|-------|-------|-------|-------|-------|-------|-------|
| PCIe/PXIe-5411 | DIO24 | DIO25 | DIO26 | DIO27 | DIO28 | DIO29 | DIO30 | DIO31 |
| DIN-68H-01 | D24 | D25 | D26 | D27 | D28 | D29 | D30 | D31 |

| | | |
|----------------|------|------|
| PCIe/PXIe-5411 | AFI6 | AFI7 |
| DIN-68H-01 | AF6 | AF7 |

Table A-1: DIN-68H-01 Pin Assignment

All jumpers on DIN-68H-01 are used for the setting of pull-up or pull-down resistor termination. The proper termination setting can reduce signal reflection during high-speed data transfer. The below diagram is the schematic of AF6, AF7, and D0 to D31. The default jumper setting of DIN-68H-01 is set to 50Ω pull-down termination. When you change the jumper setting to 5V pull-up termination, you have to apply +5V power to +5V_{IN} connector. If you don't want to set termination on specific channels, just remove the corresponding jumpers on the DIN-68H..

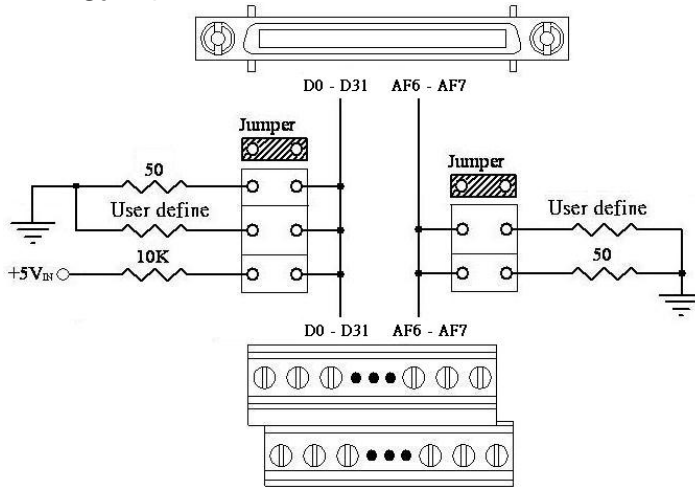


Figure A-2: Resistor Termination Schematic

The DIN-68H-01 also provides the option of user define pull-up resistor termination. Please note that the pad position of the resistor is on the back side of PCB and the resistor footprint is 1206 packaging. Below is the layout of the back side PCB and reference table of user-defined resistor termination.

| | | | | | | | | |
|----------|-----|-----|-----|-----|-----|-----|-----|-----|
| Channel | D0 | D1 | D2 | D3 | D4 | D5 | D6 | D7 |
| Resistor | R71 | R72 | R79 | R80 | R87 | R88 | R97 | R98 |

| | | | | | | | | |
|----------------|-----|-----|-----|-----|-----|-----|-----|------|
| PCIe/PXle-5411 | D8 | D9 | D10 | D11 | D12 | D13 | D14 | D15 |
| DIN-68H-01 | R73 | R74 | R81 | R82 | R89 | R90 | R99 | R100 |

| | | | | | | | | |
|----------------|-----|-----|-----|-----|-----|-----|------|------|
| PCIe/PXle-5411 | D16 | D17 | D18 | D19 | D20 | D21 | D22 | D23 |
| DIN-68H-01 | R75 | R76 | R83 | R84 | R91 | R92 | R101 | R102 |

| | | | | | | | | |
|----------------|-----|-----|-----|-----|-----|-----|------|------|
| PCIe/PXle-5411 | D24 | D25 | D26 | D27 | D28 | D29 | D30 | D31 |
| DIN-68H-01 | R77 | R78 | R85 | R86 | R93 | R94 | R103 | R104 |

| | | |
|----------------|-----|-----|
| PCIe/PXle-5411 | AF6 | AF7 |
| DIN-68H-01 | R95 | R96 |

Table A-2: Pad Position of User-Defined Resistor Termination

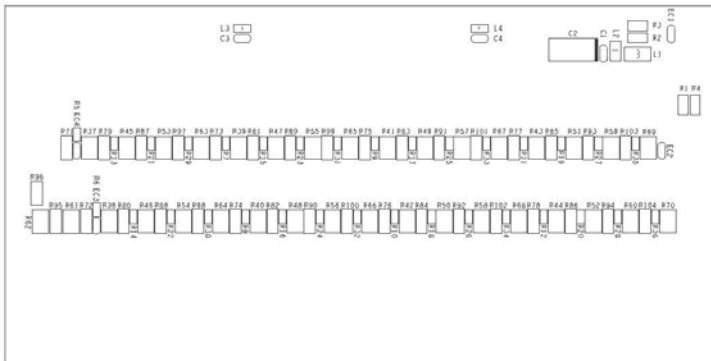


Figure A-3: DIN-68H -01 Layout (Back Side)