# PXIe-3161

## 3U PXIe Intel Xeon W-11865MRE system controller



#### Introduction

The JYTEK PXIe-3161 PXI Express embedded controller, based on the 11th gen Intel® Xeon® processor, is specifically designed for hybrid PXI Express-based testing systems, delivering maximum computing power for a wide variety of testing and measurement applications. By utilizing the Intel® Xeon® W-11865MRE processor (8 cores, 16 threads) with up to 64 GB of 3200 MT/s DDR4 memory, the PXIe-3161 easily performs execution of numerous independent tasks simultaneously in a multi-tasking environment. With an auto configure PCIe switch, the PXIe-3161 can support four links x4 or two links x8 PXI Express link capability, with maximum system throughput up to 16 GB/s by PCI Express 3.0 bus.

The JYTEK PXIe-3161 provides ample interface flexibility, including two DisplayPort connectors, allowing connection to two monitors, one USB 3.2 Gen2 10Gbps connection for high speed peripheral device, dual Ethernet ports, with one 2.5GbE for high speed LAN connection and the other for controlling LXI instruments, four USB 2.0 ports for peripheral devices and USB instrument control, and a Micro-D GPIB connector for GPIB instrument

#### Features

- 11th Gen Intel Xeon W-11865MRE processor (codename "Tiger Lake")
- 8 cores and 16 threads
- Up to 64 GB dual channel DDR4 memory at 3200 MT/s (ECC memory)
- Maximum system throughput up to 16 GB/s by PCI Express 3.0 bus
- Supports four links x4 or two links x8 PXI Express link indent to PXI Express chassis
- Integrated I/O
  - 1 USB 3.2 Gen1 5Gbps port and 1 USB 3.2 Gen2 10Gbps port
  - 4 USB 2.0
  - Dual Ethernet port: 2.5GbE port and GbE port
  - 1 RS-232/422/485 port
  - Built-in GPIB (IEEE488) controller
- 操作系统
  - Windows 10
  - Linux 64-bit

#### Special Operating Restriction

- For display connections, it is recommended that customers first use a DP (3161 end) to DP (display end) cable. If this is not possible, consider using a DP (3161 end) to HDMI (display end) cable.
   Currently, DP (3161 end) to VGA (display end) cables are not recommended (this may be updated in the future if resolved).
- BIOS Boot Mode: Only UEFI mode is supported; Legacy mode is not supported.

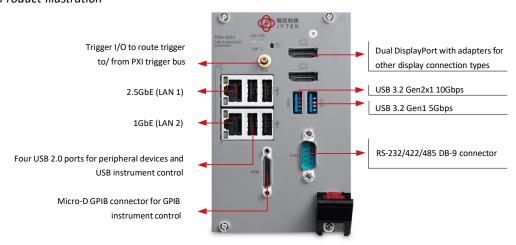
#### Ordering Information

PXIe-3161 (PN: JY8068465-01)
PXIe-3161 PXIe Intel Xeon W-11865MRE processor with
16GB memory & 500GB SSD, Windows 10 professional

#### Accessory

ACL-IEEE488-MD1-A
 25-pin Micro-D to GPIB cable, 1m

#### Product Illustration



### Specifications

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Model Name	PXIe-3161
Core Features	
СРИ	Intel® Xeon® W-11865MRE 2.6GHz
	(Turbo 4.7GHz)
DMI	DMI 3.0 8GT/s
Chipset	Intel® RM590E
Memory	Up to 64 GB dual channel DDR4 memory at 3200 MT/s (ECC memory)
Display	, (222
DisplayPort	3840 x 2160 @60Hz DisplayPort adapters to other standards are available, w/ max. resolution dependent on adapter. Support only DisplayPort to DVI or HDMI, but not support DisplayPort VGA.
PXI Express Chassis I/O	
Bus	PCI Express 3.0 (back wards compatible with 2.x and 1.x)
System Bandwidth	Up to 16GB/s
PXIe Link Configuration	2 Link Mode: x8 x8
	4 Link Mode: x4 x4 x4 x4
I/O Connectivity	
Storage	One SATA 6.0 Gb/s port with a 2.5" SATA drive bracket
Ethernet	Intel® Ethernet controller I225-IT, I210
USB	1 x USB 3.2 Gen1 , 1 x USB 3.2 Gen2 and 4 x USB 2.0, front-mounted
GPIB	Onboard IEEE488 GPIB controller
	Micro-D 25-pin connector, front-mounted (ACL-IEEE488-MD1-A cable required)
Trigger I/O	SMB connector, front-mounted, to route an external trigger signal to/from PXI trigger bus
Mechanical and Environmental	
Dimensions	3U/4-slot PXI standard
Slot Requirements	1 system slot plus 3 controller expansion slots
Weight	1 kg (2.2 lbs)
Operating Temp.	0°C to 55°C (32°F to 131°F) w/ SSD 0°C to 50°C (32°F to 122°F) w/ HDD
Storage Temp.	-20°C to 70°C (-4°F to 158°F)
Relative Humidity	5% to 95%, non-condensing
Shock	30 G, half-sine, 11 ms pulse duration
Vibration	Operating: 5 to 500 Hz, 0.21 GRMS, 3 axes
	Non-operating: 5 to 500 Hz, 2.46 GRMS, 3 axes
Emissions Compliance	EEN 61326-1, FCC Class B
CE Compliance	Immunity: EN 61326-1
Operating System	Windows 10, Linux 64-bit

## Functional Block Diagram

